## Remarks

The drawings were objected to under 37 CFR 1.83(a). Applicant herewith encloses new drawings, Figures 21 and 22, which are believed to overcome this objection. Figure 21 shows a P-channel transistor as indicated by the direction of the arrow on the line going from the transistor to the back bias control circuit. Otherwise, Figure 21 is the same as Figure 15. Figure 22 shows "a level voltage shift circuit for shifting the input voltage to said transistor." Figure 22 is a combination of Figure 13 and Figure 15.

Claims 28 and 29 were rejected under 35 U.S.C. 112, first paragraph. Claim 28 has been amended to overcome this rejection and Claim 29 has been cancelled.

As all of the objections and rejections are now believed to be overcome, reconsideration and allowance of all of the claims is requested.

Respectfully,

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